

2016 RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing

RISP
NCSP'16
Nonlinear Circuits, Communication and Signal Processing

Hilton Waikiki Beach Hotel,
Honolulu, Hawaii
March 6 – 9, 2016

Workshop Committee

General Chair

Mikio Hasegawa (Tokyo University of Science)

General Vice-Chair

Masashi Unoki (JAIST)

General Secretaries

Gou Hosoya (Tokyo University of Science)

Kohei Ohno (Meiji University)

General Vice-Secretary

Kaori Kuroda (Tokyo University of Science)

TPC Chair

Shigeki Shiokawa (Kanagawa Institute of Technology)

TPC Secretaries

Takayuki Kimura (Nippon Institute of Technology)

Isamu Matsunami (University of Kitakyushu)

Finance Chairs

Hiroaki Kurokawa (Tokyo University of Technology)

Yusuke Kozawa (Tokyo University of Science)

Publication Chair

Takafumi Matsuura (Nippon Institute of Technology)

Publicity Chair

Akira Nakamura (Tokyo University of Science)

Conference Management System Chair

Hisashi Aomori (Chukyo University)

Advisory Board

Takashi Yahagi (RISP)

Keikichi Hirose (The University of Tokyo)

Mamoru Tanaka (Sophia Univ.)

Nozomu Hamada (MJIIT UTM/Keio University)

Masato Akagi (JAIST)

Yoshifumi Nishio (Tokushima University)

Hiroyuki Yashima (Tokyo University of Science)

Yasue Mitsukura (Keio University)

Tetsushi Ueta (Tokushima University)

Rubiyah Yusof (MJIIT UTM)

Website

<http://www.risp.jp/NCSP16/>

Contact Us

[http://www.risp.jp/NCSP16/](http://www.risp.jp/NCSP16/Contact.html)

[Contact.html](http://www.risp.jp/NCSP16/Contact.html)

Organizer

The Research Institute of Signal Processing Japan
(RISP)

Call For Papers

The 2016 RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing (NCSP'16) will be held at **Hilton Waikiki Beach Hotel**, Honolulu, Hawaii, **March 6 to 9, 2016**.

The workshop is open to researchers from all over the world. In particular, the organizing committee encourages students to present their preliminary results that are not necessarily ready for publications in technical journals. Papers describing original works in all aspects of Nonlinear Circuits, Communications and Signal Processing are invited. The technical program will cover (but not limited to) the following topics.

Adaptive Systems

Bifurcation and Chaos

Circuit Design and Analysis

Complex Networks and Systems

Control and Fuzzy Systems

Estimation

Evolutionary Computation

Image Processing and Recognition

Intelligent Transport Systems

Medical Applications

Modulation and Equalization

Nonlinear Circuits and Systems

Optimization

Power and Energy Systems

Sensor Network

Speech Processing and Recognition

Spread Spectrum

Ultra Wideband

Watermarking

Ad-hoc Network

Bio-Signal Processing

Cognitive Radio

Computer Vision

Detection

Error Control Coding

Filter

Information Security

Learning System

Modeling

Neural and Bio-Inspired Networks

Optical Communications

Oscillation

Ranging and Positioning

Software Defined Radio

Speech Synthesis

Synchronization

Video Signal Processing

Wireless Communications

Paper Submission Information

Authors are invited to submit 1-page summaries through the workshop website. After the acceptance, authors will be required to submit the final paper (4-pages are required) for the workshop proceedings.

(Up to 4-pages: No charge. Page charges: 5,000yen per page up to 8-pages)

Special Session

Proposals of organized special session are welcome and should be sent email to tpc16@ncsp.jp until **December 1, 2015**.

Important Dates

Deadline for 1-page summary submission

December 15, 2015

Deadline for Special Session proposals

December 1, 2015

Notification of acceptance for Regular Session Papers

January 1, 2016

Notification of acceptance for Special Sessions

January 1, 2016

Deadline for submission of all camera-ready papers

January 15, 2016

Deadline for author registration

January 15, 2016

In cooperation with

IEEE Tokyo Young Professionals Affinity Group